EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	34	fuse adj macros	US-PGPUB; USPAT	OR	ON	2006/02/28 14:56
L2	805676	interface	US-PGPUB; USPAT	OR	ON	2006/02/28 14:56
L3	10	1 and 2	US-PGPUB; USPAT	OR	ON	2006/02/28 14:57
L4	11	hathaway-david-james.in.	US-PGPUB; USPAT	OR	ON	2006/02/28 14:58
L5	0	urish-steven-joseph.in. -	US-PGPUB; USPAT	OR	ON	2006/02/28 14:57

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = HATHAWAY
First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08049699	5508937	150	04/16/1993	INCREMENTAL TIMING ANALYSIS	HATHAWAY, DAVID J.
08316519	5636372	150	09/30/1994	NETWORK TIMING ANALYSIS METHOD WHICH ELIMINATES TIMING VARIATIONS BETWEEN SIGNALS TRAVERSING A COMMON CIRCUIT PATH	HATHAWAY, DAVID J.
08383338	5535145	150	02/03/1995	DELAY MODEL ABSTRACTION	HATHAWAY, DAVID J.
08430670	5737580	150	04/28/1995	WIRING DESIGN TOOL IMPROVEMENT FOR AVOIDING ELECTROMIGRATION BY DETERMINING OPTIMAL WIRE WIDTHS	HATHAWAY, DAVID J.
08547686	5740067	250	10/19/1995	METHOD FOR CLOCK SKEW COST CALCULATION	HATHAWAY, DAVID J.
08549405	5745735	250	10/26/1995	LOCALIZED SIMULATED ANNEALING	HATHAWAY, DAVID J.
08597743	5757657	150	02/07/1996	ADAPTIVE INCREMENTAL PLACEMENT OF CIRCUITS ON VLSI CHIP	HATHAWAY, DAVID J.
08696555	5963728	250	08/14/1996	METHOD TO PARTITION CLOCK SINKS INTO NETS	HATHAWAY, DAVID J.
08938659	5944834	150	09/26/1997	TIMING ANALYSIS METHOD FOR PLLS	HATHAWAY, DAVID J.
09004813	6202192	150	01/09/1998	DISTRIBUTED STATIC TIMING ANALYSIS	HATHAWAY, DAVID J.
09213675	6425110	150	12/17/1998	INCREMENTAL DESIGN TUNING AND DECISION MEDIATOR	HATHAWAY, DAVID J.
09467208	6615395	150	12/20/1999	METHOD FOR HANDLING	HATHAWAY,

				COUPLING EFFECTS IN STATIC TIMING ANALYSIS	DAVID J.
09475799	6751744	150	12/30/1999	METHOD OF INTEGRATED CIRCUIT DESIGN CHECKING USING PROGRESSIVE INDIVIDUAL NETWORK ANALYSIS	HATHAWAY, DAVID J.
09616551	6300809	150	07/14/2000	Double-edge-triggered flip-flop providing two data transitions per clock cycle	HATHAWAY, DAVID JAMES
09750884	6479974	150	12/28/2000	STACKED VOLTAGE RAILS FOR LOW-VOLTAGE DC DISTRIBUTION	HATHAWAY, DAVID JAMES
09780558	6795951	150	02/09/2001	METHOD AND SYSTEM FOR FAULT-TOLERANT STATIC TIMING ANALYSIS	HATHAWAY, DAVID JAMES
10711430	Not Issued	41	09/17/2004	METHOD AND APPARATUS FOR EFFICIENT UTILIZATION OF ELECTRONIC FUSE SOURCE CONNECTIONS	HATHAWAY, DAVID JAMES
11162586	Not Issued	25	09/15/2005	METHOD FOR RETICLE SHAPES ANALYSIS AND CORRECTION	HATHAWAY, DAVID JAMES
08944857	6308302	150	10/06/1997	A SEMICONDUCTOR WIRING TECHNIQUE FOR REDUCING ELECTROMIGRATION	HATHAWAY, DAVID JAMES
09532675	Not Issued	161	03/21/2000	Notebook clip	HATHAWAY, DAVID P.
09419546	Not Issued	161	10/18/1999	NOTEBOOK CLIP	HATHAWAY, DAVID PETER
10091258	Not Issued	95	03/05/2002	COMPOSITIONS AND METHODS FOR TREATING PERIPHERAL VASCULAR DISEASE	HATHAWAY, DAVID R.
10317126	Not Issued	41	12/11/2002	Methods and compositions for treating polycystic ovary syndrome	HATHAWAY, DAVID R.
10629649	Not Issued	41	07/30/2003	Methods and compositions for treating polycystic ovary syndrome	HATHAWAY, DAVID R.
10740146	Not Issued	30	12/17/2003	Prevention and treatment of cardiac arrhythmias	HATHAWAY, DAVID R.
10741534	Not Issued	30	12/19/2003	Compositions for the treatment and prevention of nephropathy	HATHAWAY, DAVID R.
11198527	Not	41	08/05/2005	Compositions and methods for	HATHAWAY,

	Issued			treating peripheral vascular disease	DAVID R.
60350395	Not Issued	159	01/22/2002	Methods and compositions for treating polycystic ovary syndrome	HATHAWAY, DAVID R.
60434508	Not Issued	159	12/17/2002	GLP-1 for prevention of cardiac arrhythmias	HATHAWAY, DAVID R.
60434888	Not Issued	159	12/19/2002	GLP-1 for treatment of nephropathy	HATHAWAY, DAVID R.
06574230	Not Issued	163	01/26/1984	GUIDE FOR TRIMMING WALL COVERINGS	HATHAWAY, DAVID R.
07652161	Not Issued	166	02/07/1991	INHIBITION OF CELL PROLIFERATION BY HYDROPHOBIC PEPTIDES	HATHAWAY, DAVID R.
07662194	5171217	250	02/28/1991	METHOD FOR DELIVERY OF SMOOTH MUSCLE CELL INHIBITORS	HATHAWAY, DAVID R.
07682627	5116864	150	04/09/1991	METHOD FOR PREVENTING RESTENOSIS FOLLOWING RECONFIGURATION OF BODY VESSELS	HATHAWAY, DAVID R.
07862075	5306250	150	04/02/1992	METHOD AND APPARATUS FOR INTRAVASCULAR DRUG DELIVERY	HATHAWAY, DAVID R.
08000836	Not Issued	161	01/05/1993	INHIBITION OF CELL PROLIFERATION BY HYDROPHOBIC PEPTIDES	HATHAWAY, DAVID R.

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Your Search was:

Last Name = HATHAWAY
First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07963053	5304184	150	10/19/1992	APPARATUS AND METHOD FOR POSITIVE CLOSURE OF AN INTERNAL TISSUE MEMBRANE OPENING	HATHAWAY, DAVID
08465765	5720757	150	06/06/1995	APPARATUS AND METHOD FOR POSITIVE CLOSURE OF AN INTERNAL TISSUE MEMBRANE OPENING	HATHAWAY, DAVID
08935848	5810850	150	09/23/1997	APPARATUS AND METHOD FOR POSITIVE CLOSURE OF AN INTERNAL TISSUE MEMBRANE OPENING	HATHAWAY, DAVID
09158446	6132440	150	09/22/1998	APPARATUS AND METHOD FOR POSITIVE CLOSURE OF AN INTERNAL TISSUE MEMBRANE OPENING	HATHAWAY, DAVID
09553267	6348059	150	04/20/2000	Apparatus and method for positive closure of an internal tissue membrane opening	HATHAWAY, DAVID
10008781	Not Issued	83	11/13/2001	Apparatus and method for positive closure of an internal tissue membrane opening	HATHAWAY, DAVID
08194072	5476469	150	02/09/1994	APPARATUS AND METHOD FOR POSITIVE CLOSURE OF AN INTERNAL TISSUE MEMBRANE OPENING	HATHAWAY, DAVID A.
06350719	Not Issued	161	02/22/1982	VIDEO GAME CONTROL	HATHAWAY, DAVID C.
06542024	Not Issued	161	10/14/1983	PROGRAM STORAGE DEVICE	HATHAWAY, DAVID C.
06602540	Not Issued	161	04/20/1984	SECURED STORAGE TANK	HATHAWAY, DAVID C.
10143539	6560375	150	05/10/2002	VIDEO IMAGE	HATHAWAY,

				STABILIZATION AND REGISTRATION	DAVID H.
11174210	Not Issued	20	07/01/2005	Video image stabilization and registration - plus	HATHAWAY, DAVID H.
09364919	6459822	150	07/26/1999	VIDEO IMAGE STABILIZATION AND REGISTRATION	HATHAWAY, DAVID H.
60099056	Not Issued	159	08/26/1998	VIDEO IMAGE STABILIZATION AND REGISTRATION (VISAR)	HATHAWAY, DAVID H.
09617908	6536024	150	07/14/2000	METHOD AND APPARATUS FOR MAKING INTEGRATED CIRCUITS HAVING GATED CLOCK TREES	HATHAWAY, DAVID J.
09650399	6557151	150	08/29/2000	DISTRIBUTED STATIC TIMING ANALYSIS	HATHAWAY, DAVID J.
09682473	Not Issued	99	09/06/2001	CLOCK SIGNAL DISTRIBUTION UTILIZING DIFFERENTIAL SINUSOIDAL SIGNAL PAIR	HATHAWAY, DAVID J.
09682707	6651229	150	10/09/2001	GENERATION OF REFINED SWITCHING WINDOWS IN STATIC TIMING ANALYSIS	HATHAWAY, DAVID J.
09713571	6609228	150	11/15/2000	LATCH CLUSTERING FOR POWER OPTIMIZATION	HATHAWAY, DAVID J.
09713829	6792582	150	11/15/2000	CONCURRENT LOGICAL AND PHYSICAL CONSTRUCTION OF VOLTAGE ISLANDS FOR MIXED SUPPLY VOLTAGE DESIGNS	HATHAWAY, DAVID J.
09750969	6687883	150	12/28/2000	SYSTEM AND METHOD FOR INSERTING LEAKAGE REDUCTION CONTROL IN LOGIC CIRCUITS	HATHAWAY, DAVID J.
09781369	6985004	150	02/12/2001	WIRING OPTIMIZATIONS FOR POWER	HATHAWAY, DAVID J.
09899413	6718523	150	07/05/2001	REDUCED PESSIMISM CLOCK GATING TESTS FOR A TIMING ANALYSIS TOOL	HATHAWAY, DAVID J.
09928573	6711719	150	08/13/2001	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION IN VLSI CIRCUIT DESIGNS	HATHAWAY, DAVID J.
09976698	6778999	150	10/12/2001	METHOD FOR	HATHAWAY,

				DISTRIBUTING A SET OF OBJECTS IN COMPUTER APPLICATION	DAVID J.
10078876	6986090	150	02/20/2002	METHOD FOR REDUCING SWITCHING ACTIVITY DURING A SCAN OPERATION WITH LIMITED IMPACT ON THE TEST COVERAGE OF AN INTEGRATED CIRCUIT	HATHAWAY, DAVID J.
10159921	6826733	150	05/30/2002	PARAMETER VARIATION TOLERANT METHOD FOR CIRCUIT DESIGN OPTIMIZATION	HATHAWAY, DAVID J.
10338929	Not Issued	121	01/08/2003	Method for performing timing closure on VLSI chips in a distributed environment	HATHAWAY, DAVID J.
10435824	7003747	150	05/12/2003	METHOD OF ACHIEVING TIMING CLOSURE IN DIGITAL INTEGRATED CIRCUITS BY OPTIMIZING INDIVIDUAL MACROS	HATHAWAY, DAVID J.
10436213	7010763	150	05/12/2003	METHOD OF OPTIMIZING AND ANALYZING SELECTED PORTIONS OF A DIGITAL INTEGRATED CIRCUIT	HATHAWAY, DAVID J.
10665273	Not Issued	71	09/18/2003	System and method for correlated process pessimism removal for static timing analysis	HATHAWAY, DAVID J.
10667083	6988255	150	09/18/2003	GENERATION OF REFINED SWITCHING WINDOWS IN STATIC TIMING ANALYSIS	HATHAWAY, DAVID J.
10709327	Not Issued	30	04/28/2004	METHOD OF IDENTIFYING PATHS WITH DELAYS DOMINATED BY A PARTICULAR FACTOR	HATHAWAY, DAVID J.
10709361	Not Issued	80	04/29/2004	METHOD AND SYSTEM FOR EVALUATING TIMING IN AN INTEGRATED CIRCUIT	HATHAWAY, DAVID J.
10709362	Not Issued	41	04/29/2004	SYSTEM AND METHOD OF ANALYZING TIMING EFFECTS OF SPATIAL DISTRIBUTION IN CIRCUITS	HATHAWAY, DAVID J.

10842589	Not Issued	41	05/10/2004	Method for tuning a digital design for synthesized random logic circuit macros in a continuous design space with optional insertion of multiple threshold voltage devices	HATHAWAY, DAVID J.
10904309	Not Issued	30	11/03/2004	SLACK SENSITIVITY TO PARAMETER VARIATION BASED TIMING ANALYSIS	HATHAWAY, DAVID J.
10905970	Not Issued	30	01/28/2005	METHOD OF CLOCK TREE DISTRIBUTION GENERATION BY DETERMINING ALLOWED PLACEMENT REGIONS FOR CLOCKED ELEMENTS	HATHAWAY, DAVID J.
10908100	Not Issued	20	04/27/2005	METHOD FOR ESTIMATING CLOCK JITTER FOR STATIC TIMING MEASUREMENTS OF MODELED CIRCUITS	HATHAWAY, DAVID J.
10908102	Not Issued	30	04/27/2005	METHOD OF GENERATING WIRING ROUTES WITH MATCHING DELAY IN THE PRESENCE OF PROCESS VARIATION	HATHAWAY, DAVID J.
10908593	Not Issued	30	05/18/2005	THE USE OF REDUNDANT ROUTES TO INCREASE THE YIELD AND RELIABILITY OF A VLSI LAYOUT	HATHAWAY, DAVID J.
11077043	Not Issued	30	03/10/2005	Hybrid linear wire model approach to tuning transistor widths of circuits with RC interconnect	HATHAWAY, DAVID J.
11095327	Not Issued	30	03/31/2005	Voltage dependent parameter analysis	HATHAWAY, DAVID J.
11162200	Not Issued	30	08/31/2005	PROCESS AND APPARATUS FOR ESTIMATING CIRCUIT DELAY	HATHAWAY, DAVID J.
11176712	Not Issued	25	07/07/2005	Wiring optimizations for power	HATHAWAY, DAVID J.
11255707	Not Issued	20	10/21/2005	Clock signal distribution utilizing differential sinusoidal signal pair	HATHAWAY, DAVID J.
11296780	Not Issued	19	12/07/2005	Method of achieving timing closure in digital integrated circuits by optimizing individual macros	HATHAWAY, DAVID J.

07127323	4916627	150	12/02/1987	LOGIC PATH LENGTH REDUCTION USING BOOLEAN MINIMIZATION	HATHAWAY, DAVID J.
07715178	5339253	250	06/14/1991	METHOD AND APPARATUS FOR MAKING A SKEW-CONTROLLED SIGNAL DISTRIBUTION NETWORK	HATHAWAY, DAVID J.
07739923	5282147	250	08/02/1991	METHOD AND APPARATUS FOR OPTIMIZING A LOGIC NETWORK	HATHAWAY, DAVID J.

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Your Search was:

Last Name = URISH
First Name = STEVEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09853115	6598206	150	05/10/2001	METHOD AND SYSTEM OF MODIFYING INTEGRATED CIRCUIT POWER RAILS	URISH, STEVEN J.
10711431	Not Issued	30	09/17/2004	METHOD AND APPARATUS FOR DEPOPULATING PERIPHERAL INPUT/OUTPUT CELLS	URISH, STEVEN J.
10711430	Not Issued	41	09/17/2004	METHOD AND APPARATUS FOR EFFICIENT UTILIZATION OF ELECTRONIC FUSE SOURCE CONNECTIONS	URISH, STEVEN JOSEPH

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